SRI ADITYA ENGINEERING COLLEGE

(Approved by AICTE & Affiliated to JNTUK, Kakinada) Aditya Nagar, ADB Road, Surampalem – 533437

Department of Electrical & Electronics Engineering

Title of the Workshop/Seminar	: A TWO DAYS WORKSHOP ON "POWER CONSERVATION FOR SMART GRIDS"
Resource Persons	: 1) Dr. Sarat Kumar Sahu,
	MVGR College of Engineering, Vijayanagaram.
	2) Dr. J.Sridevi,
	GRIET, Hyderabad.
	3) Dr. D.Vijaya Kumar,
Aditya Inst. of Technology & Management, Tekkali	
Date(s) of Workshop/Seminar	: 13-12-2013 to 14-12-2013
Aim of Workshop	:

Introducing the concepts of power conservation using smart grids

Program Schedule:

13-12-2013(Day-1):

Session 1:

• Introduction to delegates

Session 2:

• Comparison study of static voltage stability analysis methods for FACTS devices in smart grid

14-12-2013(Day-2):

Session 1:

• Zonal power quality improvement for indian utility system

Session 2:

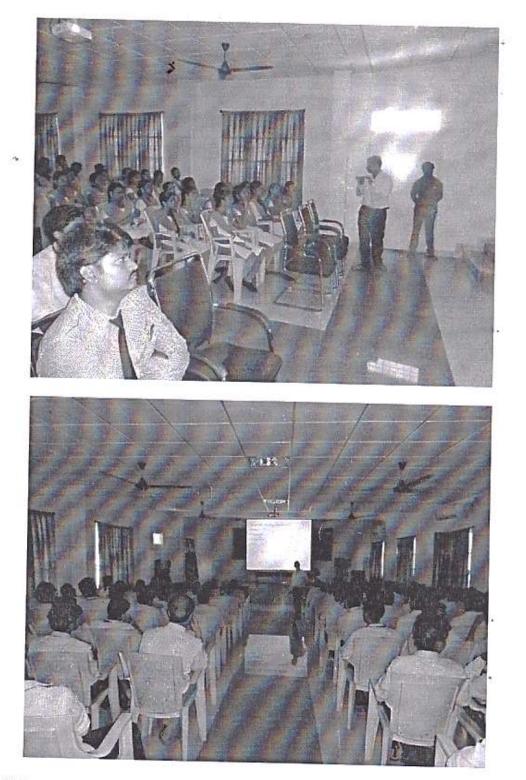
• Soft Computing Applications to Power Systems

No. of Participants

: 60



PROGRAM PHOTOS



Overall Assessment : Good

Event Coordinators :1.Mr.K.MANOZ KUMAR REDDY, Dept. of EEE - Convener

- 2. Dr.Nitimalla Raja Sekhar, Professor, Dept. of EEE
- 3. Ms.G.RatnaKumari, Assoc. Professor, Dept. of EEE
- 4. Mr.P.Bala Krishna, Asst. Professor, Dept. of EEE

5. Mr. K.K.PAVAN KUMAR, Asst. Professor, Dept. of EEE

• Sri Aditya Engineering College SURAMPALEM-533 437 Head of the Department Electrical & Electronics Engineering SRI ADITYA ENGINEERING COLLEC SURAMPALEM 533 437